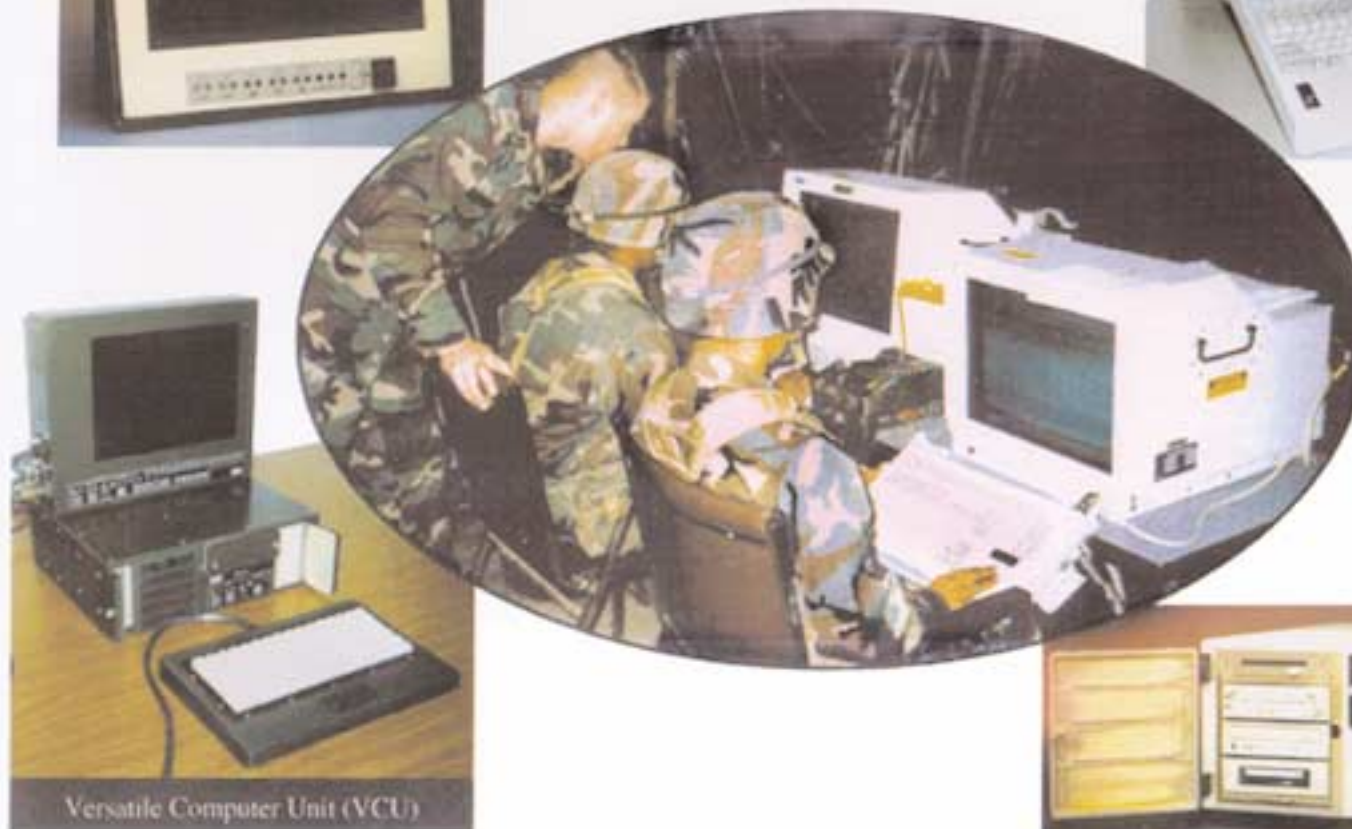


Common Hardware Systems (CHS)

20" Color Flat Panel Display (CFPD)



Compact Computer Unit-2 (CCU-2)



Versatile Computer Unit (VCU)



High Capacity Computer Unit-2 (HCU-2)



MISSION

Provide common hardware systems and common software (including common operating environment (COE) software) for the Army Battle Command System (ABCS) which includes the Global Command and Control System–Army (GCCS-A), Army Tactical Command and Control Systems (ATCCS), and Force XXI Battle Command Brigade and Below (FBCB2).

DESCRIPTION AND SPECIFICATIONS

The Common Hardware Systems (CHS) program improves interoperability and lowers life-cycle costs by standardizing battlefield command and control (C2) automation through centralized buys of non-developmental items (NDI), standardized protocols, and the development of reusable common software (CS). The program provides CHS to over 80 Army and Department of Defense customers. The CHS-2 and lightweight computer unit (LCU) programs provide the following hardware: CHS-2 ultra computer unit (UCU), handheld terminal unit (HTU), high capacity computer unit (HCU), compact computer unit (CCU), versatile computer unit (VCU), color flat panel display (CFPD), notebook computer unit (NCU), and the LCU and tactical communications interface module (TCIM) for interface to tactical radios. These contracts provide commercial, ruggedized and highly ruggedized hardware versions of computers and peripherals. They also provide commercial industry-based logistics support that meets the unique requirements of the tactical military units.

CHS/LCU software: UNIX-POSIX; RDBMS; GKS, PHIGS, PEX; DoD Protocols; GOSIP; E-MAIL; NIX, NFX, DCE; MPN/DDN X.28; ADA; DOS; PURGING SW; CASE TOOLS.

	CHS-2(HTU)	CHS-2(HCU-2)	CHS-2(CCU-2)
Processor:	80586	UltraSPARC IIi	UltraSPARC IIi/Axi
MHz clock:	133	333	333
RAM:	16 or 32MB	32MB–1GB	1GB
	CHS-2(VCU-2)	CHS-2(VCU-1)	CHS-2(NCU)
Processor:	UltraSPARC IIi/Axi	Pentium III	Pentium MMX
MHz clock:	333	500	266
RAM:	1GB	1GB	32–160MB
	CHS-2(UCU)	LCU	
Processor:	UltraSPARC II	Pentium	
MHz clock:	300	200	
RAM:	64MB–1GB	16–128MB	

The CHS hardware can be procured in Version 1 (commercial workstations), Version 2 (ruggedized workstations), and Version 3 (MIL-SPEC handheld terminal unit and LCU/TCIM), in addition to commercial off-the-shelf counterparts.

FOREIGN COUNTERPART

No known foreign counterpart

FOREIGN MILITARY SALES

None

PROGRAM STATUS

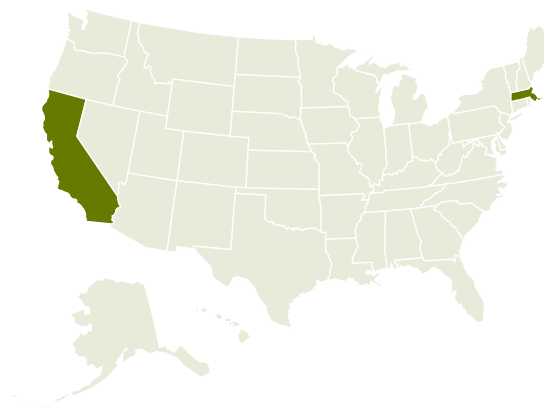
April 10, 1995 General Dynamics (formerly GTE) awarded CHS-2 contract, a follow-on to the CHS-1 contract. Version 2 hardware is fully qualified and was successfully used during both the Army Warfighter Experiment (AWE) Task Force XXI exercise and the Division AWE (DAWE) at Ft. Hood, TX.

PROJECTED ACTIVITIES

2QFY01 Commence CHS-2 Option Year 2.
3QFY01 LCU last order.

PRIME CONTRACTORS

CHS-2: General Dynamics (Taunton, MA)
LCU: Litton (San Diego, CA)



* See appendix for list of subcontractors



Project the Force



Protect the Force



Gain Information
Dominance



Shape the Battlespace



Conduct Decisive
Operations



Sustain the Force